# ZETTLER DISPLAYS

## SPECIFICATIONS FOR LIQUID CRYSTAL DISPLAY

	CUSTOMER APP	PROVAL	
X PART N	O.:_ATM0700D6J-T (Z	<u>ZETTLER DISPI</u>	AYS) VER1.3
APPROVAL		COMPANY CHOP	
CUSTOMER			
COMMENTS			

AZ DISPLAYS ENGINEERING APPROVAL							
DESIGNED BY CHECKED BY APPROVED BY							

## **REVISION RECORD**

REVISION	REVISION DATE	PAGE	CONTENTS
VER1.0	2014-09-05		FIRST ISSUE
VER1.1	2015-07-01	3,11	CORRECT VIEWING ANGLE
		6	ADD TP DRAWING
VER1.2	2015-11-11	7	CORRECT AVDD/VCOM VALUE
VER1.3	2016-11-16	7	CORRECT MAXIMUM LED FORWARD CURRENT

## **※ CONTENTS**

- 1. GENERAL SPECIFICATIONS
- 2. PIN ASSIGNMENT
- 3. OPERATING SPECIFICATIONS
- 4. OPTICAL SPECIFICATIONS
- 5. RELIABILITY TEST
- 6. PRECAUTION FOR USING LCM
- 7. MECHANICAL DRAWING
- 8. PACKAGE DRAWING
- 9. INSPECTION SPECIFICATION

## **1. GENERAL SPECIFICATIONS**

Item		Specification	Remark
1. LCD siz	ze	7.0 inch(Diagonal)	
2. Driver e	element	a-Si TFT active matrix	
3. Resolut	ion	800x(RGB)x480	
4. Display	mode	Normally white, Transmissive	
5. Dot Pito	≿h (W*H)	0.0642mm(W) x 0.1790mm(H)	
6. Pixel pit	ch(W*H)	0.1926mm(W) x 0.1790mm(H)	
7. Active	Area(W*H)	154.08mm(W) x 85.92mm(H)	
8. Module	size (W*H)	165.0mm(W) x 100.0mm(H) x 7.1mm(D)	Note 1
9. Surface	e treatment	Anti-glare	
10. Bezel n	naterial	Stainless steel	
11. Color a	rrangement	RGB-stripe	
12. View D	irection	12 O'Clock	
12. Color		16.7M	
13. Interfac	e .	24bit RGB interface	
14. Weight		210g(Typ.)	
15. RoHS		ROHS compliant	

Note 1: Please refer to mechanical drawing.

## 2. PIN ASSIGNMENT

#### **TFT LCD Panel Driving Section**

FPC Connector is used for the module electronics interface. The recommended model is FH12A-50S-0.5SH manufactured by Hirose.

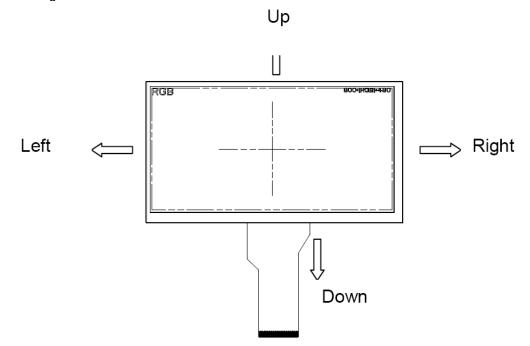
Pin No.	Symbol	Function	Level	Note
1,2	$V_{LED^+}$	Power for LED backlight(Anode)	Р	
3,4	V <sub>LED-</sub>	Power for LED backlight(Cathode)	Р	
5	GND	Power ground	Р	
6	V <sub>COM</sub>	Common voltage	Р	
7		Power for digital circuit	Р	
8	MODE	DE/SYNC mode select	I	Note 1
9	DE	Data input enable	Ι	
10	VS	Vertical sync input	_	
11	HS	Horizontal sync input	-	
12~17	B7~B2	Blue data	_	
18~19	B1~B0	Blue data		Note 2
20~25	G7~G2	Green data	_	
26~27	G1~G0	Green data	_	Note 2
28~33	R7~R2	Red data		
34~35	R1~R0	Red data	I	Note 2
36	GND	Power ground	Р	
37	DCLK	Sample clock	Ι	Note 3
38	GND	Power ground	Р	
39	L/R	Left/Right selection	Ι	Note 4,5
40	U/D	Up/Down selection	I	Note 4,5
41	V <sub>GH</sub>	Gate on voltage	Р	
42	V <sub>GL</sub>	Gate off voltage	Р	
43	AV <sub>DD</sub>	Power for analog circuit	Р	
44	RESET	Global reset pin	I	Note 6
45	NC	No connection		
46	V <sub>COM</sub>	Common voltage	Р	
47	DITHB	Dithering function	I	Note 7
48	GND	Power ground	Р	
49~50	NC	No connection		

#### I: input, O: output, P: Power

Note 1: DE/SYNC mode select. Normally pull high.
When select DE mode, MODE="1", VS and HS must pull high.
When select SYNC mode, MODE= "0", DE must be grounded.
Note 2: When input 18 bits RGB data, the two low bits of R,G and B data must be grounded.
Note 3: Data shall be latched at the falling edge of DCLK.

_								
	Setting of sca	an control input	Soonning direction					
	U/D	L/R	Scanning direction					
	GND DV <sub>DD</sub>		Up to down, left to right					
	DV <sub>DD</sub> GND		Down to up, right to left					
	GND GND		Up to down, right to left					
	DV <sub>DD</sub> DV <sub>DD</sub>		Down to up, left to right					

**Note 5**: Definition of scanning direction. Refer to the figure as below:



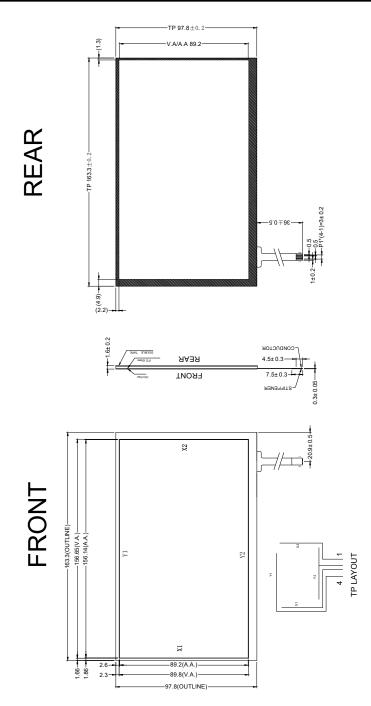
**Note 6**: Global reset pin. Active low to enter reset state. Suggest to connect with an RC reset circuit for stability. Normally pull high.

**Note 7**: Dithering function enable control, normally pull high. When DITHB="1",Disable internal dithering function, When DITHB="0",Enable internal dithering function,

#### **Touch Panel Section**

FPC Connector is used for the module electronics interface. The recommended model is FH12-4S-1SH manufactured by Hirose.

Pin No.	Symbol I/O Function		Remark	
4	Y1	Тор	Top electrode – differential analog	
3	X1	Left	Left electrode – differential analog	
2	Y2	Bottom	Bottom electrode – differential analog	
1	X2	Right	Right electrode – differential analog	



Parameters:

- Operating voltage: DC5V;
- 40~100g; Operating force: 2
- Insulation resistance > 10M  $\Omega$  ,(25VDC); **≪1.5%**; -inearity က
  - 20% ~ 90%RH; Working temperature: -20  $^\circ C$  ~ +70  $^\circ C$ 4
    - 20% ~ 85%RH **~ +80** °C Storage temperature: -20  $^\circ C$ 
      - ransmittance >75%;
- Structure: Film + Glass;
  - ITO-Film: Anti glare;
- Response time < 10ms;
  - electrode resistance: 200~900 ohms; 1008701
    - Unspecified tolerance:+/-0.3mm
- 6

## 3. Operating Specification

### 3.1 ABSOLUTE MAXIMUM RATINGS

ltem	Symbol	Val	ues	Unit	Remark
item	Symbol	Min. Max.		Unit	Remark
		-0.3	5.0	V	
	AV <sub>DD</sub>	6.5	13.5	V	
Power Voltage	V <sub>GH</sub>	-0.3	40	V	
	V <sub>GL</sub>	-20	0.3	V	
	$V_{GH}$ - $V_{GL}$		40	V	
Operation Temperature	T <sub>OP</sub>	-20	70	°C	
Storage Temperature	T <sub>ST</sub>	-30	80	°C	
LED Reverse Voltage	V <sub>R</sub>	-	1.2	V	Each LED Note 2
LED Forward Current	l <sub>F</sub>		35	mA	Each LED

**Note 1**: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

**Note 2**: V<sub>R</sub> Conditions: Zener Diode 20mA

#### 3.1.1 Typical Operation Conditions

ltem	Symbol		Values	Unit	Bomork	
item		Min.	Тур.	Max.	Unit	Remark
Power Voltage	DV <sub>DD</sub>	3.0	3.3	3.6	V	Note 2
	AV <sub>DD</sub>	10.2	10.4	10.6	V	
	V <sub>GH</sub>	15.3	16.0	16.7	V	
	V <sub>GL</sub>	-7.7	-7	-6.3	V	
Input Signal Voltage	V <sub>сом</sub>	3.8	4.0	4.2	V	
Input Logic High Voltage	VIH	$0.7 DV_{DD}$			V	Note 3
Input Logic Low Voltage	VIL	0		$0.3 DV_{DD}$	V	Note 3

Note 1: Be sure to apply  $DV_{DD}$  and  $V_{GL}$  to the LCD first, and then apply  $V_{GH}$ .

**Note 2**:  $DV_{DD}$  setting should match the signals output voltage (refer to Note 3) of customer's system board.

Note 3: DCLK,HS,VS,RESET,U/D, L/R,DE,R0~R7,G0~G7,B0~B7,MODE,DITHB.

#### 3.1.2 Current Consumption

Item	Symbol	۱	/alues	Unit	Remark	
item	Symbol	Min.	Тур.	Max.		Keinark
	I <sub>GH</sub>		0.2	1.0	mA	V <sub>GH</sub> =16.0V
Power Voltage	I <sub>GL</sub>		0.2	1.0	mA	$V_{GL}$ =-7.0V
Power voltage	I <sub>DVDD</sub>		4.0	10	mA	DV <sub>DD</sub> =3.3V
	I <sub>AVDD</sub>		20	50	mA	AV <sub>DD</sub> =10.4V

#### 3.1.3 Backlight driving conditions

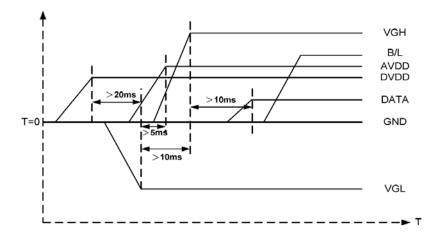
Item	Symbol	Values		Unit	Remark	
item	Symbol	Min.	Тур.	Max.	Unit	Kennark
Voltage for LED Backlight	VL	9.0	10	11	V	Note 1
Current for LED Backlight	١L		320		mA	
LED life time		30000			Hr	Note 2

**Note 1**: The LED Supply Voltage is defined by the number of LED at Ta=25 $^{\circ}$ C and I<sub>L</sub> =320mA. **Note 2**: The "LED life time" is defined as the module brightness decrease to 50% original brightness at

Ta=25  $^\circ\!\mathrm{C}$  and IL =320mA.

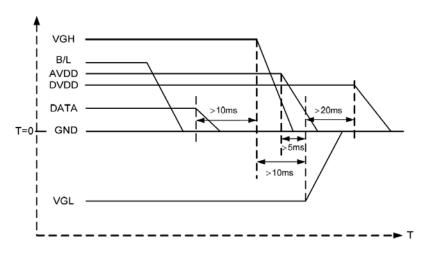
#### 3.2 Power Sequence

#### a.Power on:









 $B/L{\rightarrow}Data{\rightarrow}V_{GH}{\rightarrow}V_{GL}{\rightarrow}DV_{DD}$ 

Note: Data include R0~R7, B0~B7, GO~G7, U/D, L/R, DCLK, HS,VS,DE.

## **3.3 Timing Characteristics**

### **3.3.1 AC Electrical Characteristics**

Item	Symbol	l v	/alues	Unit	Remark	
item	Symbol	Min.	Тур.	Max.		Reinark
HS Setup Time	Thst	8			ns	
HS Hold Time	Thhd	8			ns	
VS Setup Time	Tvst	8			ns	
VS Hold Time	Tvhd	8			ns	
Data Setup Time	Tdsu	8			ns	
Data Hold Time	Tdhd	8			ns	
DE Setup Time	Tesu	8			ns	
DE Hold Time	Tehd	8			ns	
DV <sub>DD</sub> Power On Slew Time	TPOR			20	ms	From 0 to 90% DV <sub>DD</sub>
Reset Pulse Width	TRst	1			ms	
DCLK Cycle Time	Tcoh	20			ns	
DCLK Pulse Duty	Tcwh	40	50	60	%	

#### 3.3.2 Data Input Format



Figure 3.1 Horizontal input timing diagram.

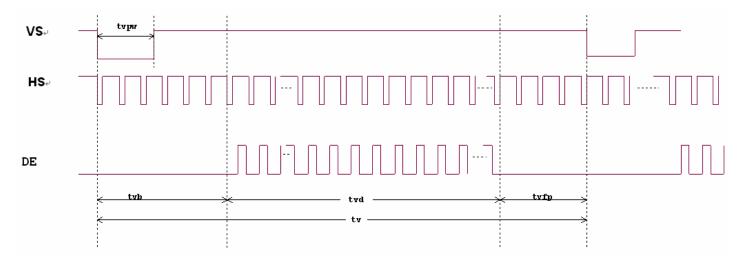


Figure 3.2 Vertical input timing diagram.

## 3.3.3 Timing

Item	Symbol	\ \	/alues	Unit	Remark	
item	Symbol	Min.	Тур.	Max.		Remark
Horizontal Display Area	thd		800		DCLK	
DCLK Frequency	fclk	26.4	33.3	46.8	MHz	
One Horizontal Line	th	862	1056	1200	DCLK	
HS Pulse Width	thpw	1		40	DCLK	
HS Blanking	thb	46	46	46	DCLK	
HS Front Porch	thfp	16	210	354	DCLK	

Item	Symbol	\ \	/alues	Unit	Remark	
item	Symbol	Min.	Тур.	Max.		Reinark
Vertical Display Area	tvd		480		TH	
VS Period Time	tv	510	525	650	ТН	
VS Pulse Width	tvpw	1		20	ТН	
VS Blanking	tvb	23	23	23	TH	
VS Front Porch	tvfp	7	22	147	TH	

## 4.0 OPTICAL SPECIFICATIONS

ltem	Symbol	Condition		Values		Unit	Remark
item	Symbol	Condition	Min.	Тур.	Max.	Onic	Remark
	θι	Φ=180°(9 Ο'CLOCK)	60	70			Note 1
Viewing Angle	θ <sub>R</sub>	Φ=0°(3 O'CLOCK)	60	70		dograa	
(CR≥10)	θτ	Ф=90°(12 О'CLOCK)	60	70		degree	
	θΒ	Φ=270°(6 Ο'CLOCK)	40	50			
Baananaa Tima	T <sub>ON</sub>			10	20	msec	Note 3
Response Time	T <sub>OFF</sub>			15	30	msec	Note 3
Contrast Ratio	CR		400	500			Note 4
	Wx	Normal		0.31			Note 2
Color Chromaticity	\ <b>\</b> /	Θ=Φ=0°		0.33			Note 5
	W <sub>Y</sub>			0.33			Note 6
Luminance	L		800			cd/m <sup>2</sup>	Note 6
Luminance Uniformity	YU		75	80		%	Note 7

Test Conditions:

1. IL=320mA (Backlight current), the ambient temperature is  $25^{\circ}$ C.

2. The test systems refer to Note 2.

Note 1: Definition of viewing angle range

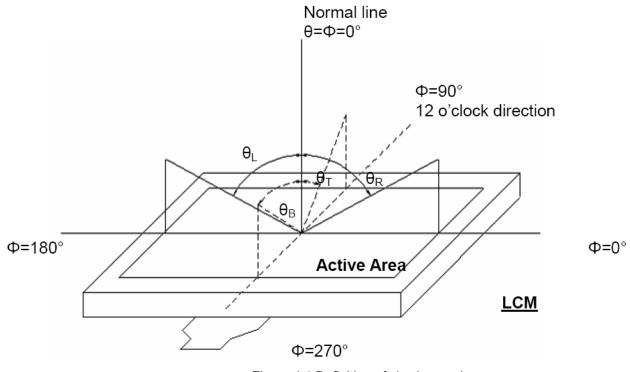


Figure 4.1 Definition of viewing angle.

#### Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON

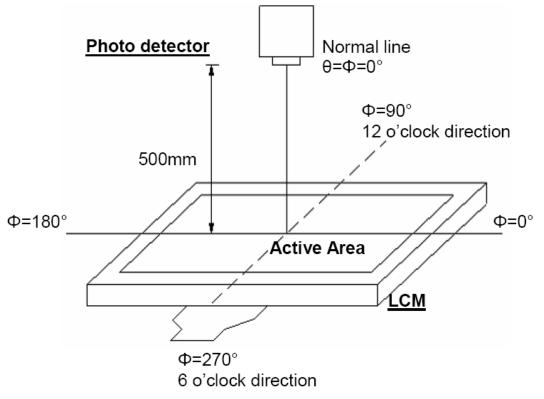


Figure 4.2 Optical measurement system setup

#### Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.

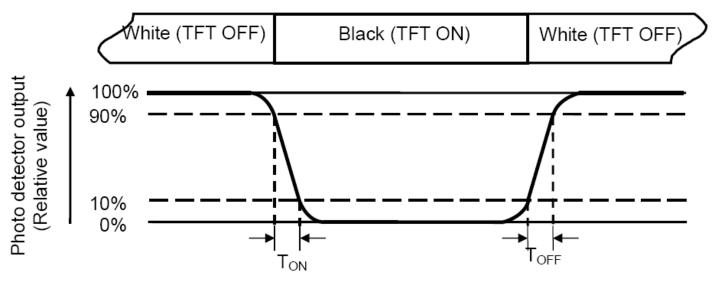


Figure 4.3 Definition of response.

#### Note 4: Definition of contrast ratio

 $Contrast ratio(CR) = \frac{Luminance measured when LCD on the "white" state}{Luminance measured when LCD on the "black" state}$ 

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

**Note 6**: All input terminals LCD panel must be ground while measuring the center area of the panel.

Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4 ). Every measuring point is placed at the center of each measuring area.

## Luminance Uniformity (Yu) = $\frac{B_{min}}{B_{max}}$

L-----Active area length

W----- Active area width

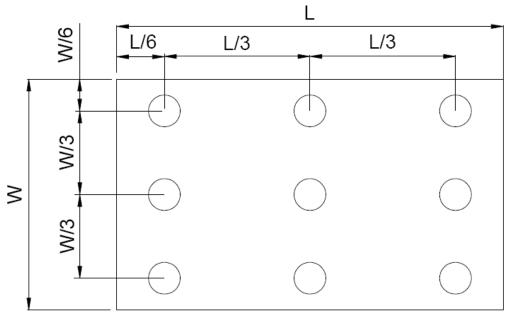


Figure 4.3 Definition of measuring points.

Bmax: The measured maximum luminance of all measurement position. Bmin: The measured minimum luminance of all measurement position.

## **5. RELIABILITY TEST**

Item	Test Condition Item	Remark
High temperature storage	Ta= 80 °C 96hrs	Note 1 Note 4
Low temperature storage	Ta=-30 °C 96hrs	Note 1 Note 4
High temperature operation	Ts= 70 °C 96hrs	Note 2 Note 4
Low temperature operation	Ts=-20 °C 96hrs	Note 1 Note 4
High temperature/High humidity operation	90% RH 60°C 96hrs	Note 4
Thermal Shock	-30°C/30 min ~ +80°C/30 min for a total 100 cycles, Start with cold temperature and end with high temperature.	Note 4
Vibration test	Freq:10~55~10Hz Amplitude:1.5mm 2 hours for each direction of X,Y,Z (6 hours for total)	
Mechanical shock	100G 6ms,±X, ±Y, ±Z 3 times for each direction	
Package vibration test	Random Vibration : 0.015G*G/Hz from 5-200HZ, -6dB/Octave from 200-500HZ 2 hours for each direction of X. Y. Z. (6 hours for total)	
Package drop test	Height:60 cm 1 corner, 3 edges, 6 surfaces	
Electro static discharge	± 2KV, Human Body Mode, 100pF/1500Ω	

Note 1: Ta is the ambient temperature of samples.

Note 2: Ts is the temperature of panel's surface.

Note 3: In the standard condition, there shall be no practical problem that may affect the

display function. After the reliability test, the product only guarantees operation,

but don't guarantee all of the cosmetic specification.

**Note 4**: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

## 6. PRECAUTION FOR USING LCM

- 1. When design the product with this LCD Module, make sure the viewing angle matches to its purpose of usage.
- 2. As LCD panel is made of glass substrate, Dropping the LCD module or banging it against hard objects may cause cracking or fragmentation. Especially at corners and edges.
- 3. Although the polarizer of this LCD Module has the anti-glare coating, always be careful not to scratch its surface. Use of a plastic cover is recommended to protect the surface of polarizer.
- 4. If the LCD module is stored at below specified temperature, the LC material may freeze and be deteriorated. If it is stored at above specified temperature, the molecular orientation of the LC material may change to Liquid state and it may not revert to its original state. Excessive temperature and humidity could cause polarizer peel off or bubble. Therefore, the LCD module should always be stored within specified temperature range.
- 5. Saliva or water droplets must be wiped off immediately as those may leave stains or cause color changes if remained for a long time. Water vapor will cause corrosion of ITO electrodes.
- 6. If the surface of LCD panel needs to be cleaned, wipe it swiftly with cotton or other soft cloth. If it is not still clean enough, blow a breath on the surface and wipe again.
- 7. The module should be driven according to the specified ratings to avoid malfunction and permanent damage. Applying DC voltage cause a rapid deterioration of LC material. Make sure to apply alternating waveform by continuous application of the M signal. Especially the power ON/OFF sequence should be kept to avoid latch-up of driver LSIs and DC charge up to LCD panel.
- 8. Mechanical Considerations
  - a) LCM are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modifications. The following should be noted.
  - b) Do not tamper in any way with the tabs on the metal frame.
  - c) Do not modify the PCB by drilling extra holes, changing its outline, moving its components or modifying its pattern.
- 9. Static Electricity
  - a) Operator

Wear the electrostatics shielded clothes because human body may be statically charged if not ware shielded clothes. Never touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.

b) Equipment

There is a possibility that the static electricity is charged to the equipment, which has a function of peeling or friction action (ex: conveyer, soldering iron, working table). Earth the equipment through proper resistance (electrostatic earth: 1x10<sup>8</sup> ohm).

Only properly grounded soldering irons should be used.

If an electric screwdriver is used, it should be well grounded and shielded from commutator sparks.

The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended.

c) Floor

Floor is the important part to drain static electricity, which is generated by operators or equipment.

There is a possibility that charged static electricity is not properly drained in case of insulating floor. Set the electrostatic earth (electrostatic earth: 1x10<sup>8</sup> ohm).

d) Humidity

Proper humidity helps in reducing the chance of generating electrostatic charges. Humidity should be kept over 50%RH. e) Transportation/storage

The storage materials also need to be anti-static treated because there is a possibility that the human body or storage materials such as containers may be statically charged by friction or peeling.

The modules should be kept in antistatic bags or other containers resistant to static for storage.

f) Soldering

Soldering anything to this TFT display would void the warranty.

g) Others

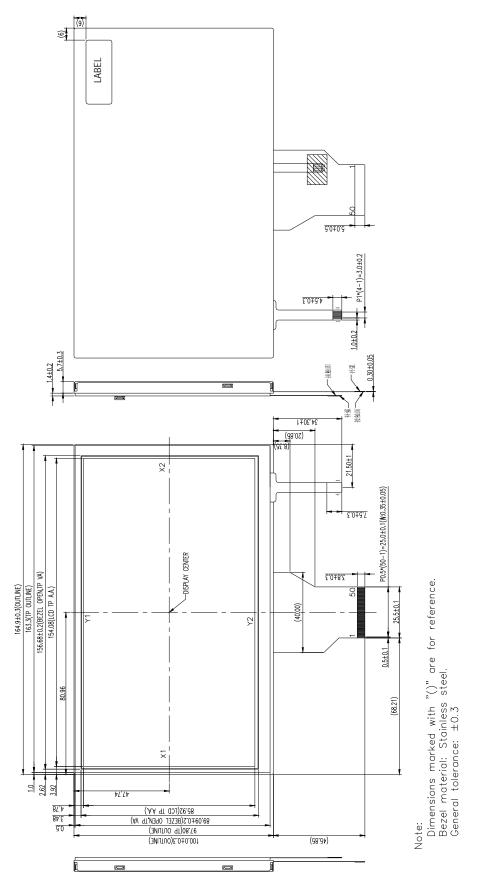
The laminator (protective film) is attached on the surface of LCD panel to prevent it from scratches or stains. It should be peeled off slowly using static eliminator.

Static eliminator should also be installed to the workbench to prevent LCD module from static charge. 10. Operation

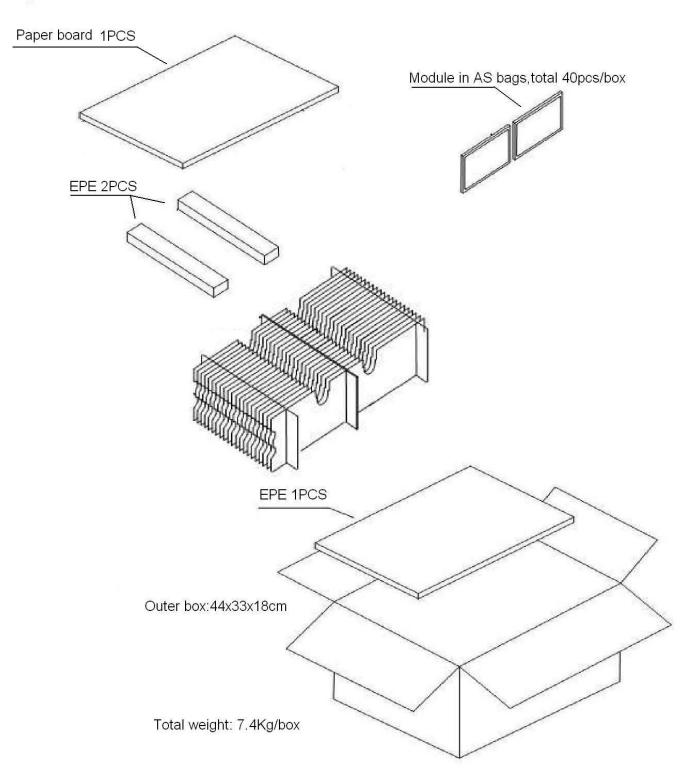
- a) Driving voltage should be kept within specified range; excess voltage shortens display life.
- b) Response time increases with decrease in temperature.
- c) Display may turn black or dark blue at temperatures above its operational range; this is (however not pressing on the viewing area) may cause the segments to appear "fractured".
- d) Mechanical disturbance during operation (such as pressing on the viewing area) may cause the segments to appear "fractured".

- 11. If any fluid leaks out of a damaged glass cell, wash off any human part that comes into contact with soap and water. The toxicity is extremely low but caution should be exercised at all the time.
- 12. Disassembling the LCD module can cause permanent damage and it should be strictly avoided.
- 13. LCD retains the display pattern when it is applied for long time (Image retention). To prevent image retention, do not apply the fixed pattern for a long time. Image retention is not a deterioration of LCD. It will be removed after display pattern is changed.
- 14. Do not use any materials, which emit gas from epoxy resin (hardener for amine) and silicone adhesive agent (dealcohol or deoxym) to prevent discoloration of polarizer due to gas.
- 15. Avoid the exposure of the module to the direct sunlight or strong ultraviolet light for a long time.

## 7. MECHANICAL DRAWING



## 8. PACKAGE DRAWING



## 9. INSPECTION SPECIFICATION

#### **1. SCOPE SPECIFICATIONS CONTAIN**

- **1.1 DISPLAY QUALITY EVALUATION**
- 1.2 MECHANICS SPECIFICATION

#### 2. SAMPLING PLAN

UNLESS THERE IS OTHER AGREEMENT, THE SAMPLING PLAN FOR INCOMING INSPECTION SHALL FOLLOW MIL-STD-105E.

- 2.1 LOT SIZE: QUANTITY PER SHIPMENT AS ONE LOT (DIFFERENT MODEL AS DIFFERENT LOT ).
- 2.2 SAMPLING TYPE: NORMAL INSPECTION, SINGLE SAMPLING.
- 2.3 SAMPLING LEVEL: LEVEL II.
- 2.4 AQL: ACCEPTABLE QUALITY LEVEL
  - MAJOR DEFECT: AQL=0.65

MINOR DEFECT: AQL=1.0

#### **3. PANEL INSPECTION CONDITION**

3.1 ENVIRONMENT:

ROOM TEMPERATURE: 25±5°C.

- HUMIDITY: 65±5% RH.
- ILLUMINATION: 300 ~ 700 LUX.
- 3.2 INSPECTION DISTANCE:

35±5 CM

3.3 INSPECTION ANGLE:

THE VISION OF INSPECTOR SHOULD BE PERPENDICULAR TO THE SURFACE OF THE MODULE.

3.4 INSPECTION TIME:

PERCEPTIBILITY TEST TIME: 20 SECONDS MAX.

#### 4. DISPLAY QUALITY

4.1 FUNCTION RELATED:

THE FUNCTION DEFECTS OF LINE DEFECT, ABNORMAL DISPLAY, AND NO DISPLAY ARE CONSIDERED MAJOR DEFECTS.

4.2 BRIGHT/DARK DOTS:

Defect Type	Specification	Major	Minor
Bright Dots	N≤ 2		•
Dark Dots	N≤ 3		•
Total Bright and Dark Dots	N≤ 4		•

Note: 1:

The definition of dot: The size of a defective dot over 1/2 of whole dot is regarded as one defective dot.

Bright dot: Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern. The bright dot defect must be visible through 2% ND filter

Dark dot: Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue pattern.

4.3 Pixel Definition:

R	G	В	R	G	В	R	G	В	Dot Defect
R	G	В	R	G	В	R	G	В	Adjacent Dot Defect
R	G	В	R	G	В	R	G	В	Cluster

Note 1:

If pixel or partial sub-pixel defects exceed 50% of the affected pixel or sub-pixel area, it shall be considered as1 defect.

Note 2:

There should be no distinct non-uniformity visible through 2% ND Filter within 2 sec inspection times.

4.4Visual Inspection specifications:

Defect	Туре	Specification Size	Count(N)	Major	Minor
Dot Shape		D ≤0.25 mm	Ignored		
	Scratch and Bubbles in	0.25mm < D ≤ 0.5mm	$N \leq 3$		
display area		D > 0.5mm	N=0		•
$\bigcirc$	Ŧ				
Nouton Di	ng (Only for Touch popul)	D≤70mm	N≤4		
	ng (Only for Touch panel)	D>70mm	N=0		•
TSD Eich E	yes (Only for Touch panel)	0.1mm <d⊴0.2mm< td=""><td></td><td></td></d⊴0.2mm<>			
	yes (Only for Toden parter)	0.2mm <d⊴0.3mm< td=""><td>N≤3</td><td></td><td>•</td></d⊴0.3mm<>	N≤3		•
(Bubble/De	nt)	0.3 <d≤0.4< td=""><td>N≤2</td><td></td><td></td></d≤0.4<>	N≤2		
Line Shap	e	$W \le 0.01 \text{ mm}$	Ignored		
	Scratch、Lint and Bubbles	0.01mm< W $\leq$ 0.05mm and L $\leq$ 3mm	$N \leq 3$		•
		W > 0.05mm or L > 3 mm N=0			
Bubble in c	ell (active area)	It should be found by eyes		•	
	Scratch			•	
Bezel	Dirt	No harm		•	
	Wrap	No harm		•	
	Sunken	No harm		•	
	No label			•	
	Inverted label	No			•
	Broken			•	
	Dirt	Word can be read.		•	
Label	Not clear				•
	Word out of shape	No			•
	Mistake	No		•	
	Position	Be attached on right positio		•	
	Not enough	No			•
Screw	Limp	No		•	

## ATM0700D6J-T(ZETTLER DISPLAYS)TFT MODULE VER1.3

Connector	Connection status	No bend on pins and damage	•
FPC/FFC	Broken	No	•

Note: Extraneous substance and scratch not affecting the display of image, for instance, extraneous substance under polarizer film but outside the display area, or scratch on metal bezel and backlight module or polarizer film outside the display area, shall not be considered as defective or non-conforming.